EUROPEAN PATENT OFFICE

Patent Abstracts of Japan

PUBLICATION NUMBER

05347422

PUBLICATION DATE

27-12-93

APPLICATION DATE

16-06-92

APPLICATION NUMBER

04156326

APPLICANT: FUJITSU LTD;

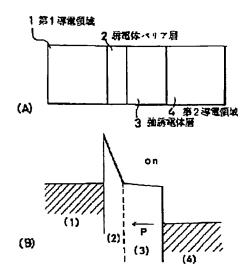
INVENTOR: TAMURA YASUTAKA;

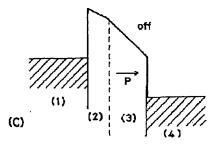
INT.CL.

: H01L 29/88 G11C 13/00 H01L 49/02

TITLE

: BISTABLE DIODE





ABSTRACT: PURPOSE: To provide a bistable diode which has a memory function and a large on/off ratio of a current.

> CONSTITUTION: At least two dielectric layers 2 and 3 are provided between a first conductive electrode 1 and a second conductive electrode 4. At least one of the dielectric layers 2 and 3 is a ferroelectric layer (for instance 3). In this construction, a barrier which prevents carriers from moving may be provided along a boundary between the dielectric layer 2 or 3 and at least one of the first and second conductive electrodes 1 and 4. Further, at least one of the first and second conductive electrodes 1 and 4 may be made of metal or oxide material showing superconducting properties.

COPYRIGHT: (C)1993,JPO&Japio